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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES****On Appeal to the Board of
Appeals and Interferences**

Appellant(s) : Jens David et al. Examiner: Thuan Du N
Serial No. : 10/525,688 Group Art Unit: 2116
Filed : July 27, 2005
Title : Method For Initializing Programmable System

REPLY BRIEF ON APPEAL

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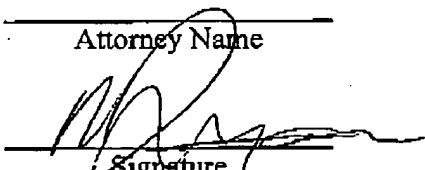
April 7, 2008
Date of Deposit

Manu J Tejawani

37,952

Attorney Name

Registration No.


Signature

April 7, 2008
Date of Signature

Mail Stop Appeal Brief - Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Appellant files this paper in response to the Examiner's Answer mailed on December
February 7, 2007. Further, Appellant requests an Oral Hearing.

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Appellant's Response to Examiner's Answer Section (10) Response to Arguments

The Examiner maintains the § 103 rejection of claims 1 and 4-10 as being obvious from Branstad and AAPA. The Examiner's argument is presented on pages 5-7 of the Answer. Applicants respectfully traverse.

Applicants submit that the Examiner's analysis of applicants' invention as defined by the claims is not correct.

Applicants' invention has the following features common with AAPA and/or Branstad.

The programmable system comprises an ASIC 1, which is completed with several elements (OEM specific) (e.g. external electronics 7, I/O interface 6, bus interface 2, etc.). The programmable system is provided with a non volatile storage medium 14 (external, i.e. outside of the programmable system, or internal, i.e. within the programmable system).

Applicants' invention has the following features that are in contrast to, and above and beyond, the combination of AAPA and Branstad.

The initialization program is divided into two parts: (a) a first (basic) program, and (b) a second (initialization) program.

The first part — program (a), can be understood to be a "basic" program or "bootstrap" program, as explained in the specification paragraph [0017]. This basic or bootstrap program is stored in instruction memory 9 coupled to (associated with) processor element 8.

The second part — program (b), is also referred to as the "initialization" program." This initialization program part is stored in storage medium 14, and controls the further initialization

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after having been transferred into instruction RAM 10 of the ASIC as explained in specification paragraph [0017].

Applicants submit that the combination of AAPA and Branstad does show applicants' two-part initialization program.

Applicants note that in AAPA, instruction is controlled by a hardware component, i.e. hardwired initialization logic 16. An initialization program is not disclosed. An initialization program is also not necessary because the initialization is controlled by the hardwired logic. The initialization information consists only of initialization data as explained in the specification paragraph [0009].

In Branstad, initialization information consisting of initialization program (init code) and initialization information is stored within the non volatile storage medium. (See Branstad col. 1, lines 35-36 and 62-65, col. 2, lines 37- 38, and col. 5, lines 32-35). In Branstad, the transfer of the initialization program from the storage medium to microcontroller 42 is controlled by a (hardwired) EEPROM control logic 50 (comparable with AAPA's EEPROM interface 12). (See Branstad col. 5, lines 19-23). The only distinction of Branstad over AAPA is the usage of an initialization program stored within the storage medium. The disadvantages of AAPA (especially as explained in applicants' specification paragraphs [0013] and [0014]) are not overcome by Branstad.

For this reason also, it is respectfully submitted that a case that the claims of the present invention are obvious has not been established.

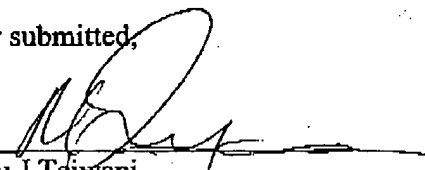
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III. Conclusion

For the reasons set forth herein, as well as for the reasons set forth in Appellant's Brief, Appellant respectfully requests reversal of the rejections and allowance of claims 1 and 4-10.

Respectfully submitted,

Dated: April 7, 2008

By: 
Manu J Tejwani
Patent Office Reg. No. 37,952

Attorney for Appellants
Telephone: (212) 408-2500

Baker Botts L.L.P.
30 Rockefeller Plaza
New York, NY 10112